

Time:	Sep 13, 09:35
Who:	Admin Icera->you
Subject:	ICERA2023 submission review request

Dear Assoc. Prof. Dr Do Nhu Y,

The Program Committee of ICERA 2023 conference would like to invite you to be a reviewer for the following paper submitted to ICERA 2023:

 Paper id: 7601

Title: A sliding-mode controlled single-phase common-ground boost inverter with low frequency current ripple reduction

ICERA 2023 has started review process, we would very much appreciate if you could promptly find the time to give a quick look at this paper and decide whether you can accept the invitation to review it. If you kindly accept the invitation, please return your report within 10 days after you accepted the invitation.

Please inform as early as possible if you agree to accept our invitation to review. We hope as part of academic community you'll appreciate our efforts to complete quality peer review within stipulated time period (as mentioned above).

The instructions on how to answer this review request can be found at the bottom of this letter or click the following address:

<https://easychair.org/account/signin>. You will have to create an EasyChair account first unless you have one already

We look forward to a positive confirmation.

Best regards,

Admin Icera <icera@tnut.edu.vn>

Time:	Oct 15, 02:00
Who:	you->Admin Icera
Subject:	Your review request for ICERA2023 submission 7601

I agree to review this submission

Your Review

Review 2	
Paper	7601
Title	A sliding-mode controlled single-phase common-ground boost inverter with low frequency current ripple reduction
Authors	Tan-Tai Tran, Minh-Cuong Nguyen, Minh-Thuyen Chau, Minh-Tan Tran and Minh-Duc Ngo
PC member	Admin Icera
Reviewer	Do Nhuy < donhuy@humg.edu.vn >
Time	Oct 18, 01:21
Overall evaluation	2: (accept) This paper presents a novel two-stage inverter configuration with a common ground setup. The introduced configuration comprises two distinct parts: the DC-DC boost section and the common-ground inverter section. Additionally, a straightforward control strategy for mitigating the output current ripple caused by the inverter is introduced. This is a novel approach on single-phase common-ground two-level boost inverters The author should add in the conclusion the results achieved after the research process.
Reviewer's confidence	4: (high)
Confidential remarks for the program committee	